

05-13-05

STJ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Kevin Bruno



Serial No. :

09/934,051

Filed :

August 21, 2001

For :

BZFLASH Subcircuit to
Dynamically Supply BZ Codes for
Controlled Impedance Buffer
Development, Verification, and
System Level Simulations

Group Art Unit :

2128

Examiner :

Jones, Hugh

Atty Docket :

/ 01-148

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

5/11/05 *Connie Del Castillo*

Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Timothy Croll

Timothy Croll

Reg. No. 36,771

Date: 11 MAY 05